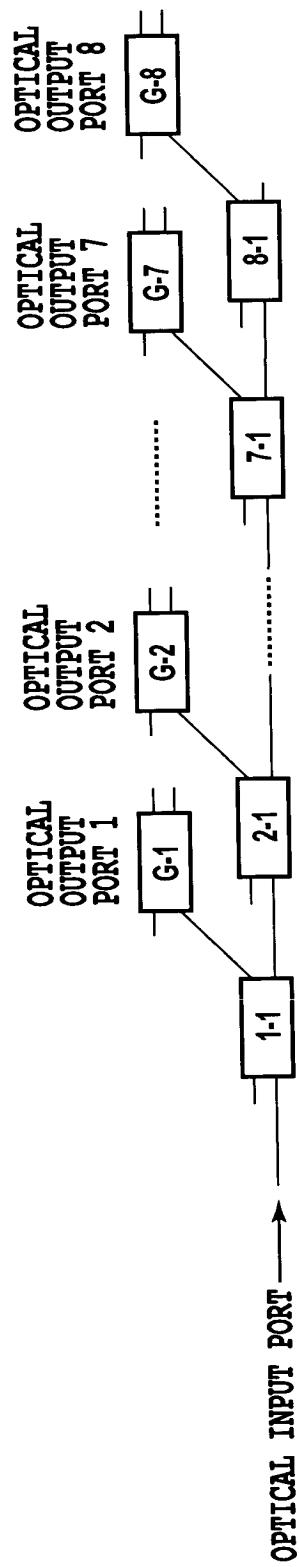
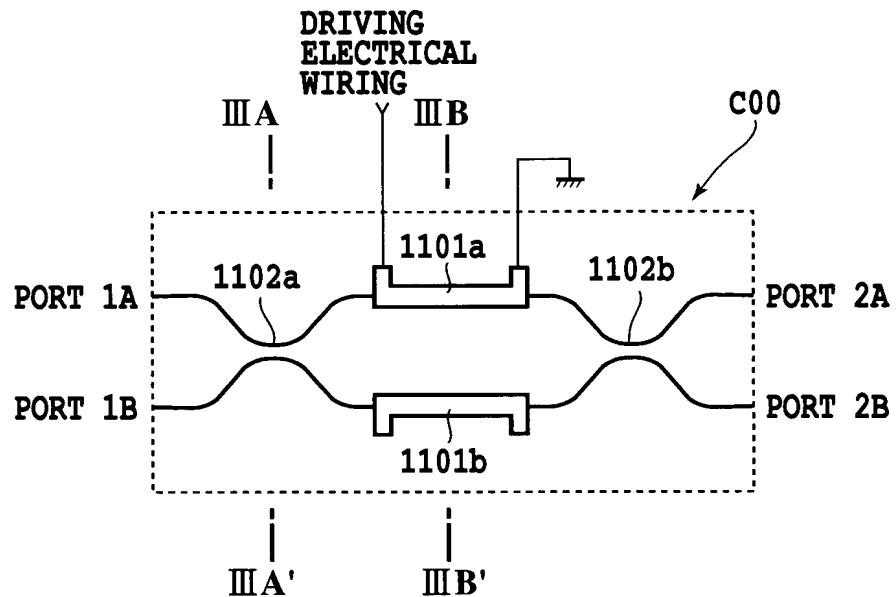


**FIG.1**  
**PRIOR ART**

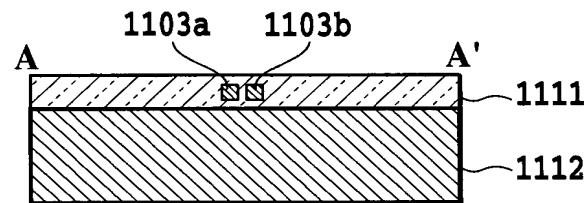


**FIG.2**  
**PRIOR ART**

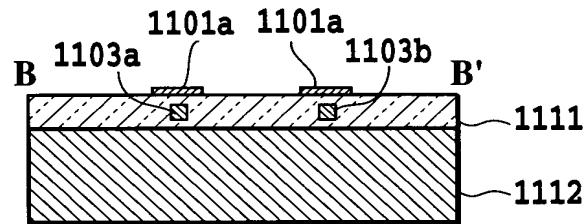
**FIG.3A**  
PRIOR ART

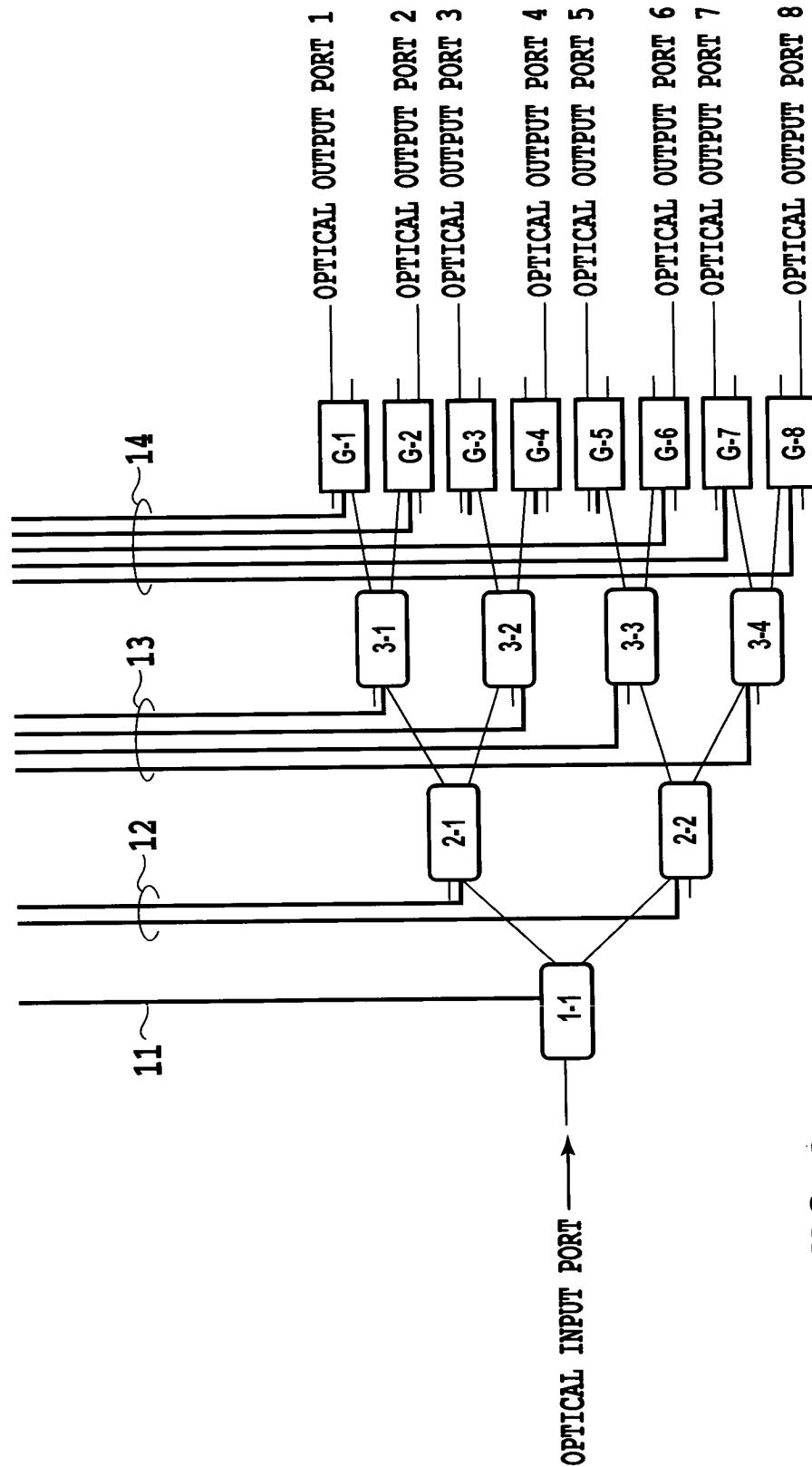


**FIG.3B**  
PRIOR ART



**FIG.3C**  
PRIOR ART





**FIG.4**

PRIOR ART

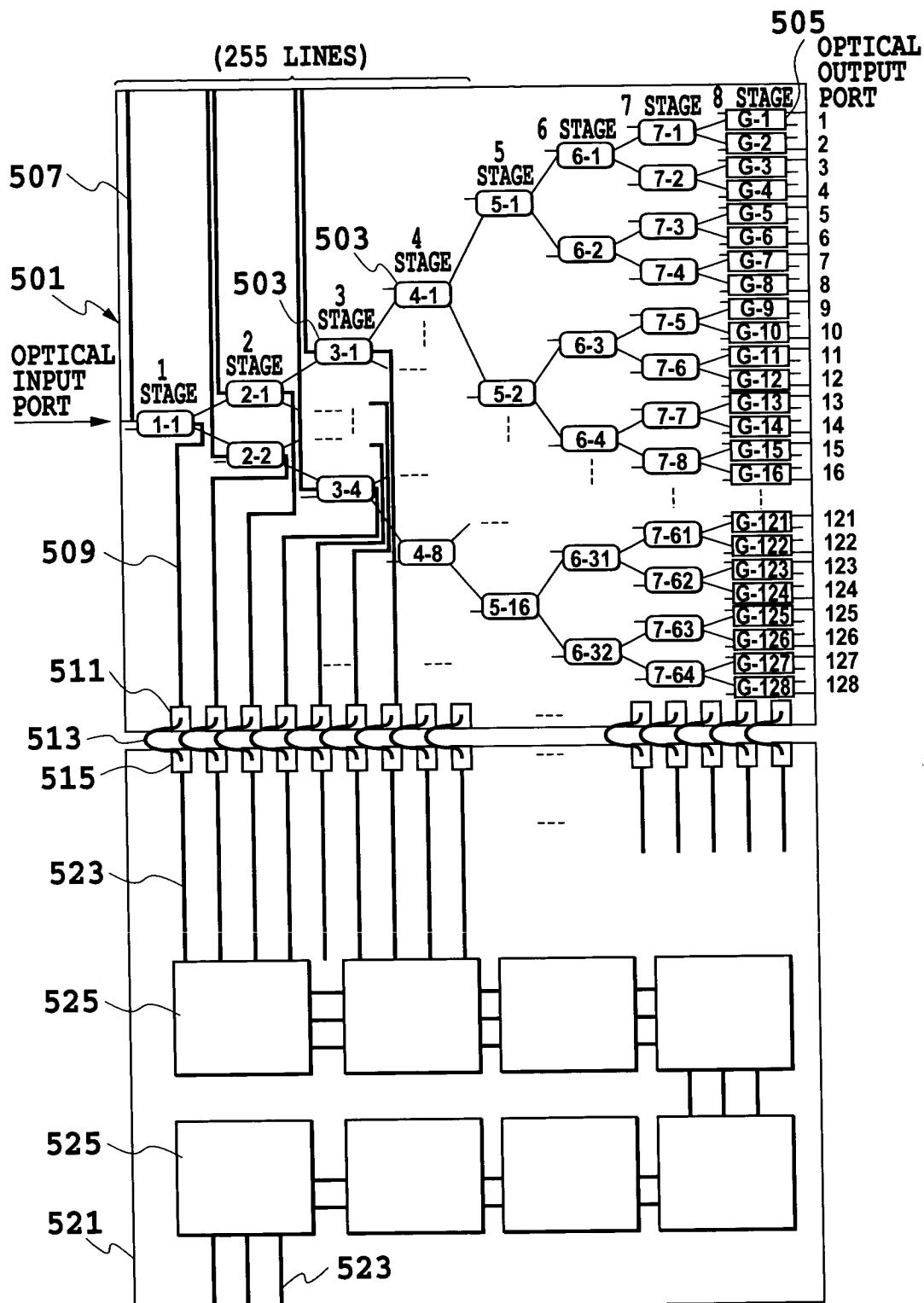
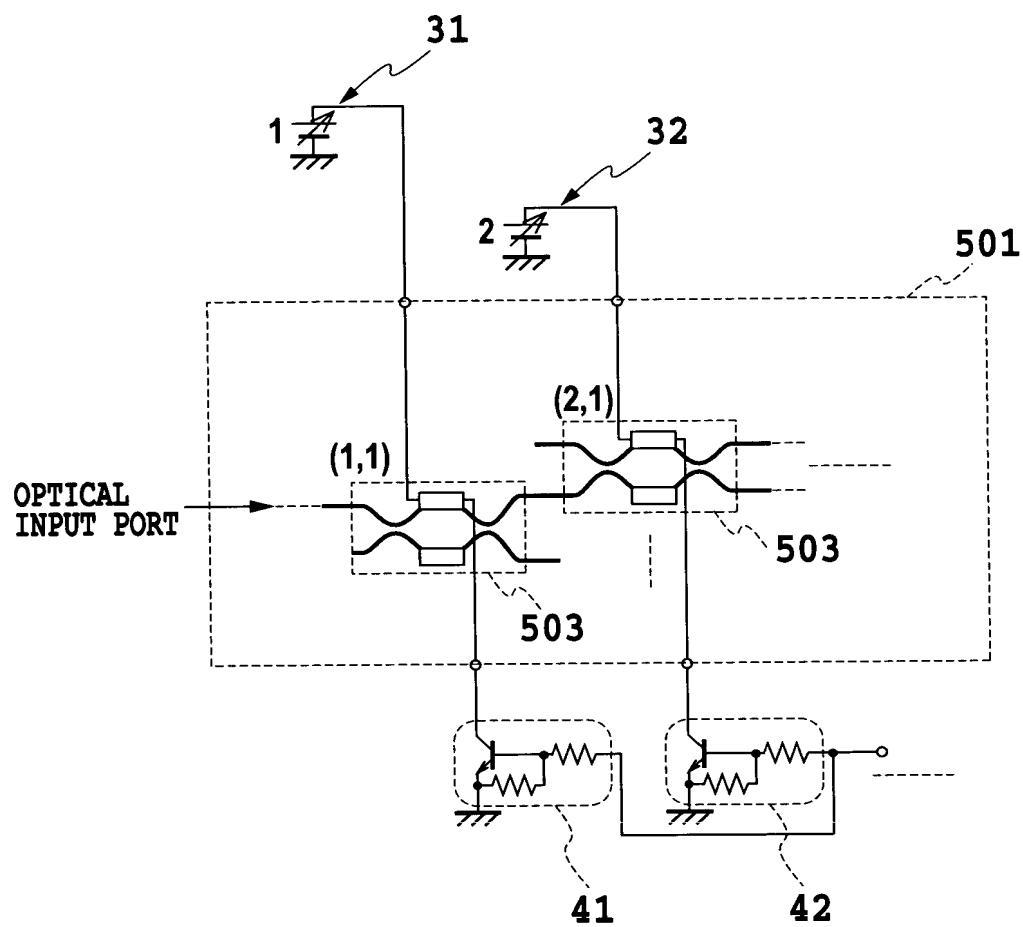


FIG.5  
PRIOR ART



**FIG.6**  
**PRIOR ART**

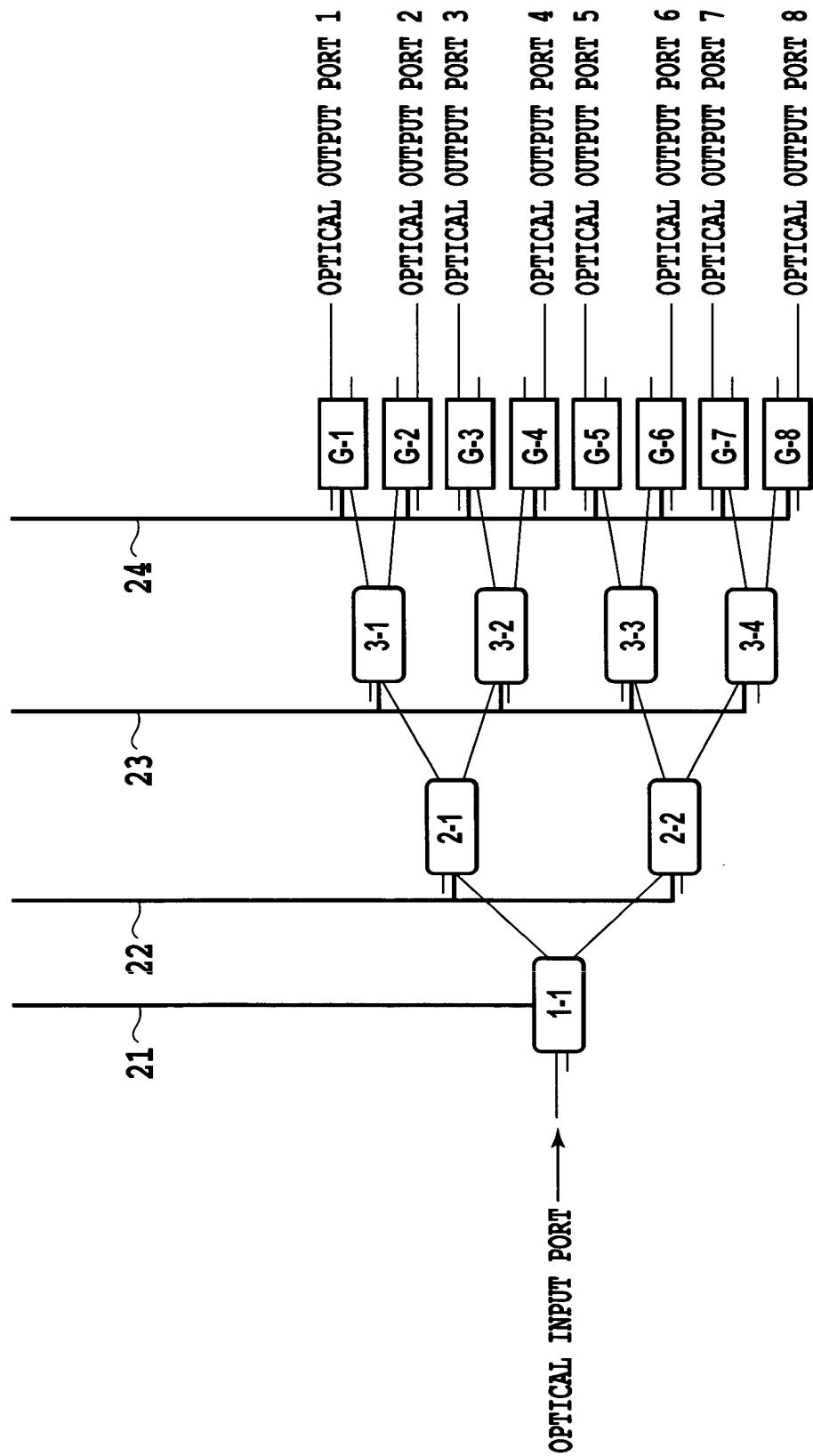
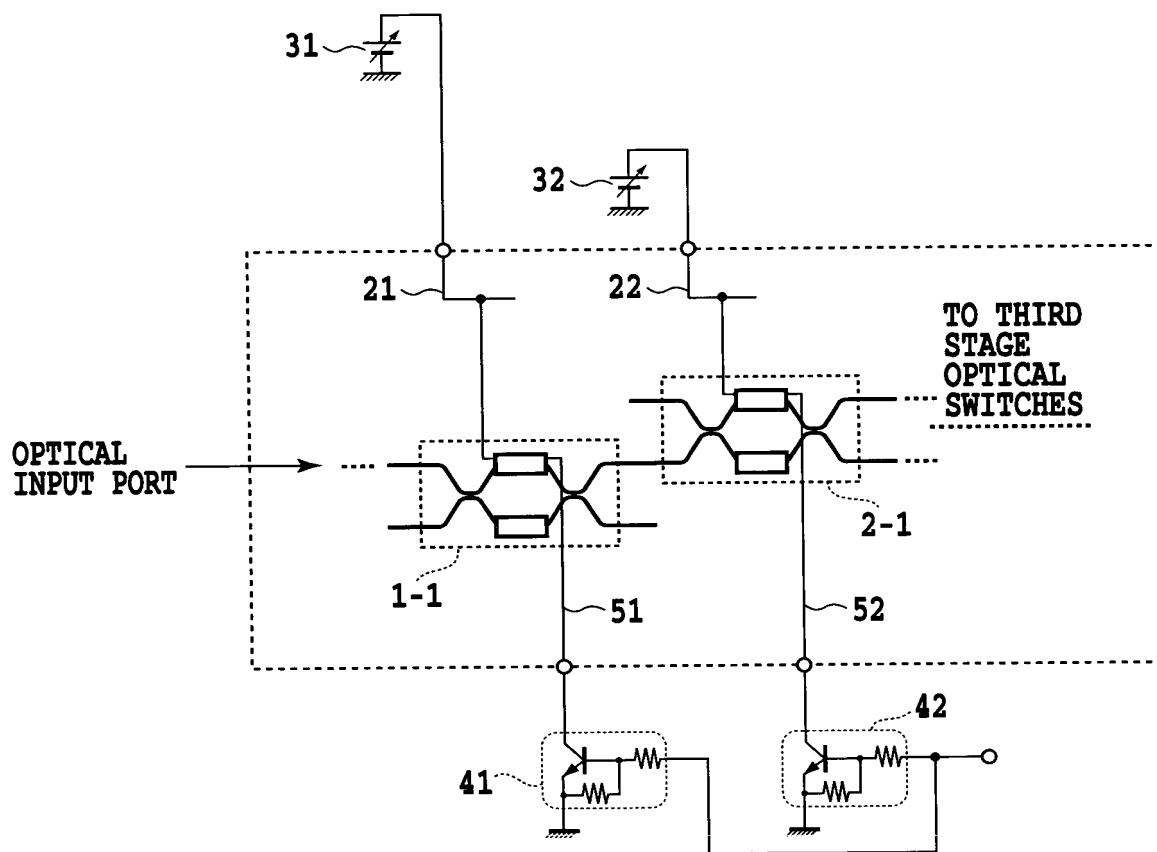


FIG.7



**FIG.8**

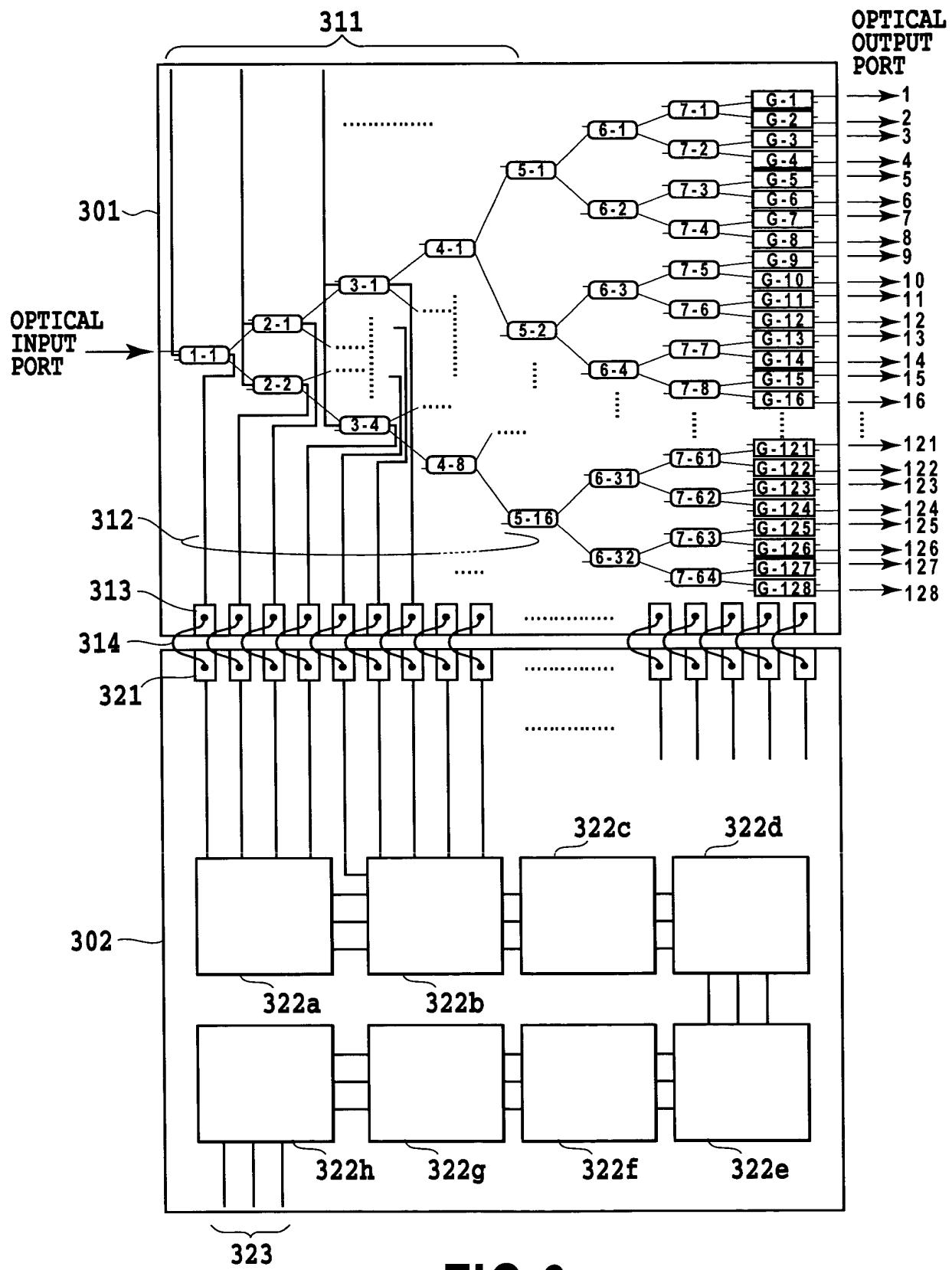


FIG.9

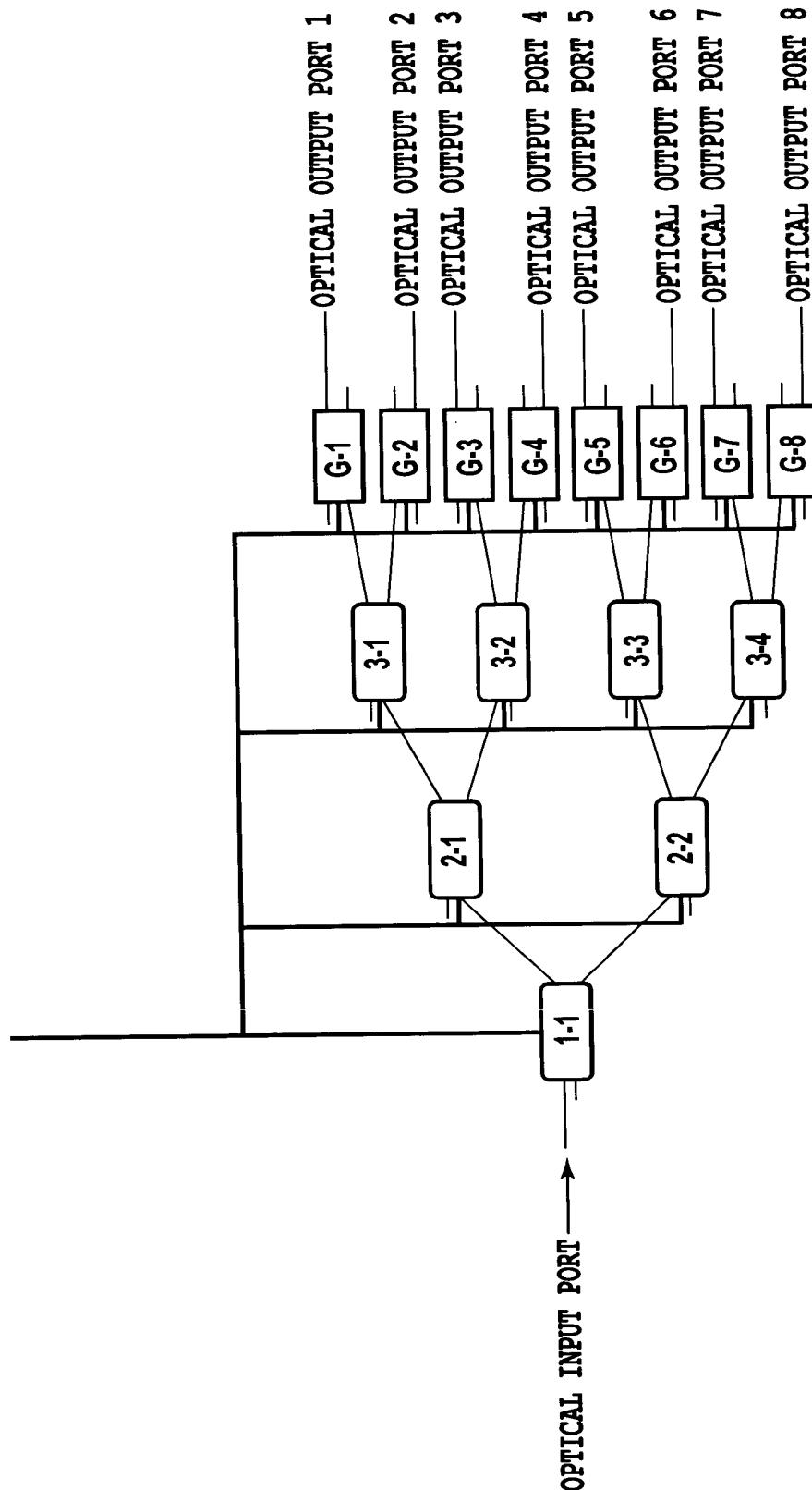


FIG.10

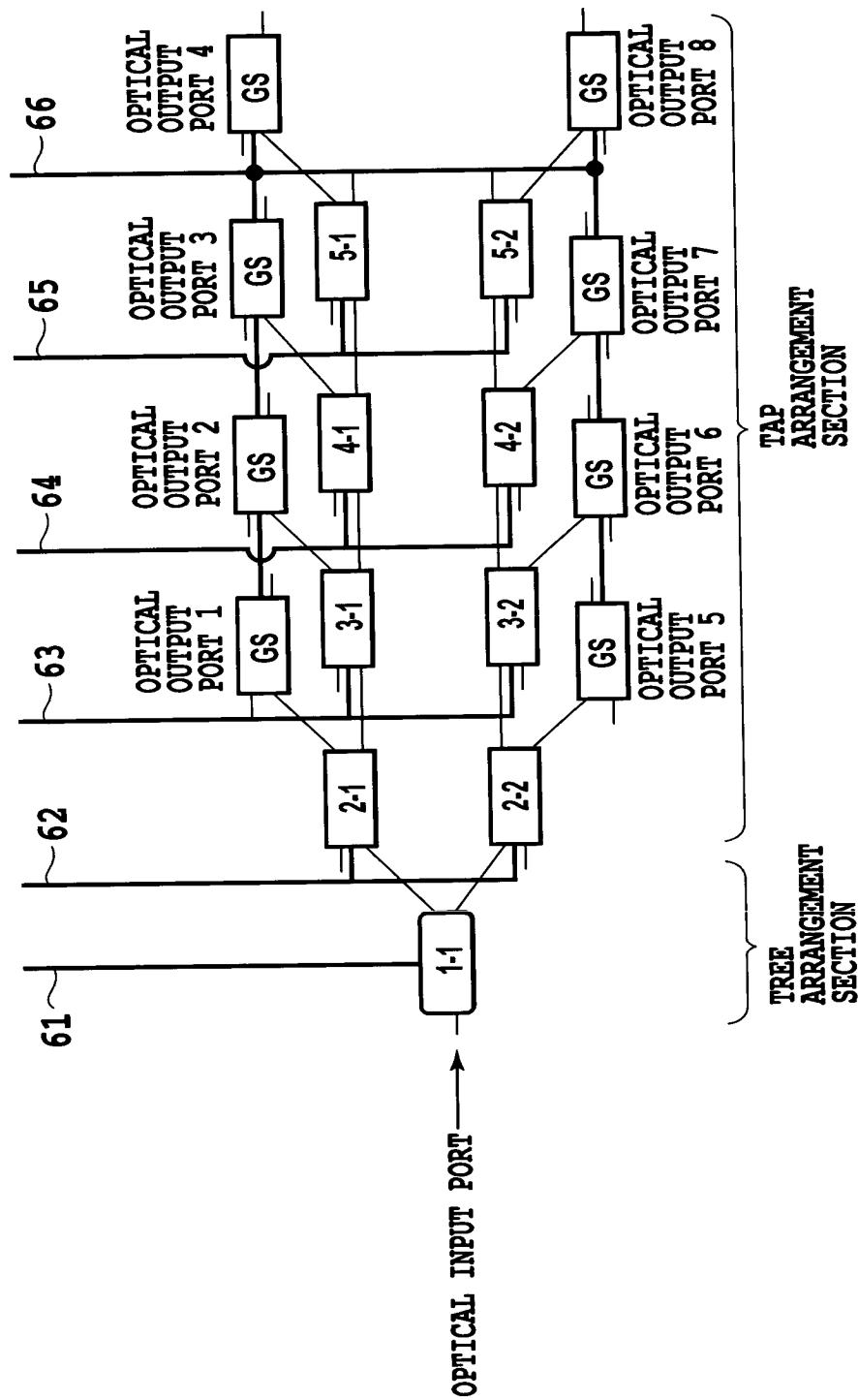


FIG.11

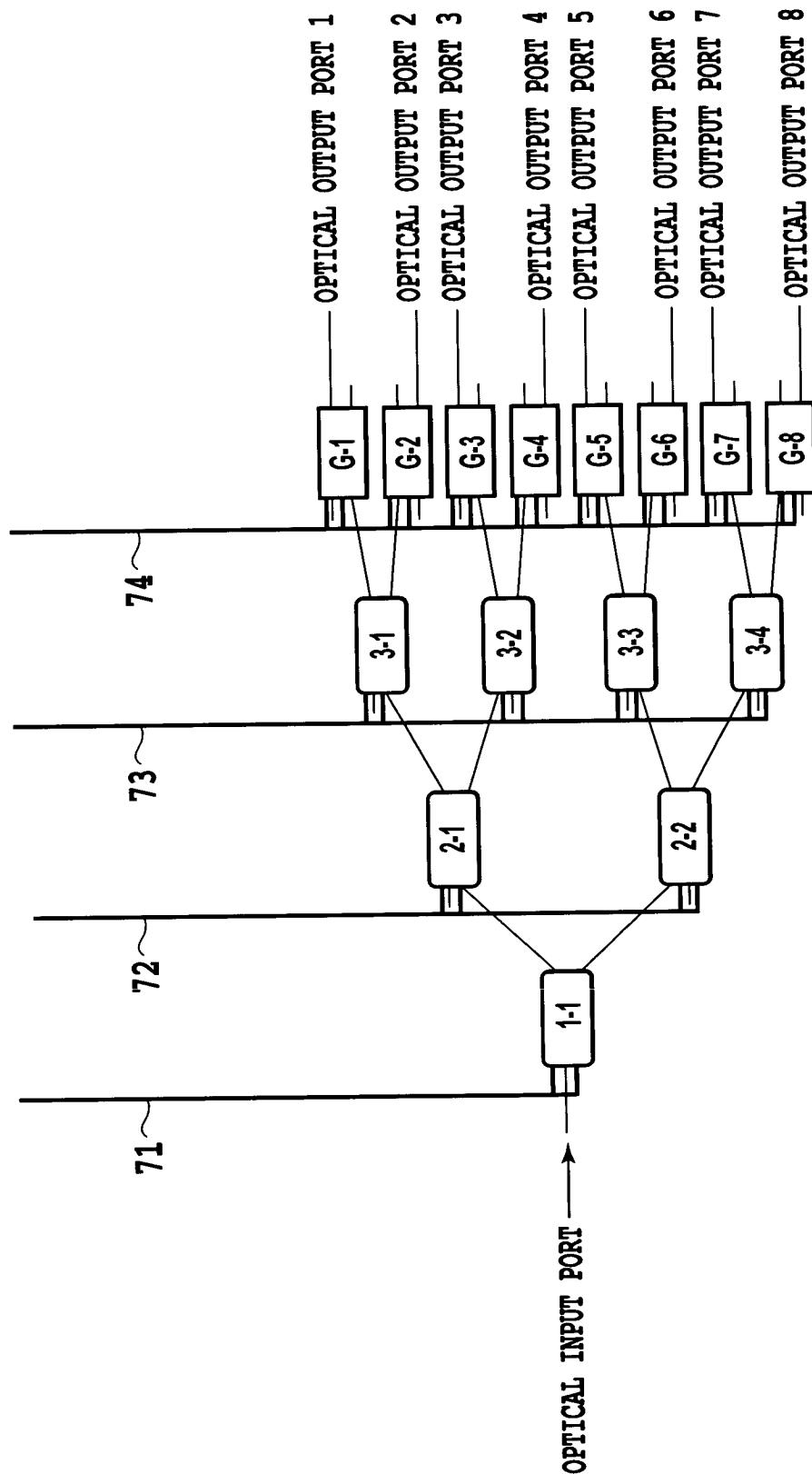


FIG. 12

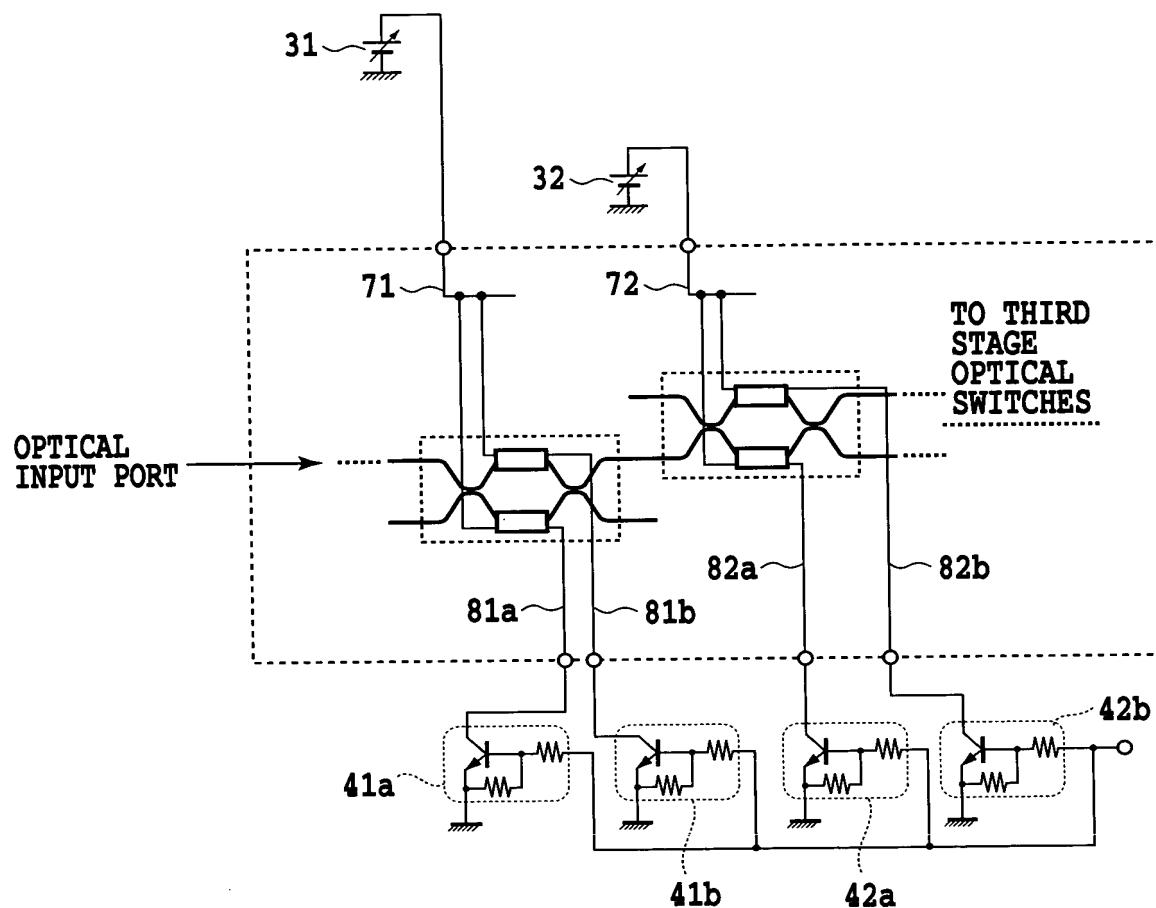


FIG.13

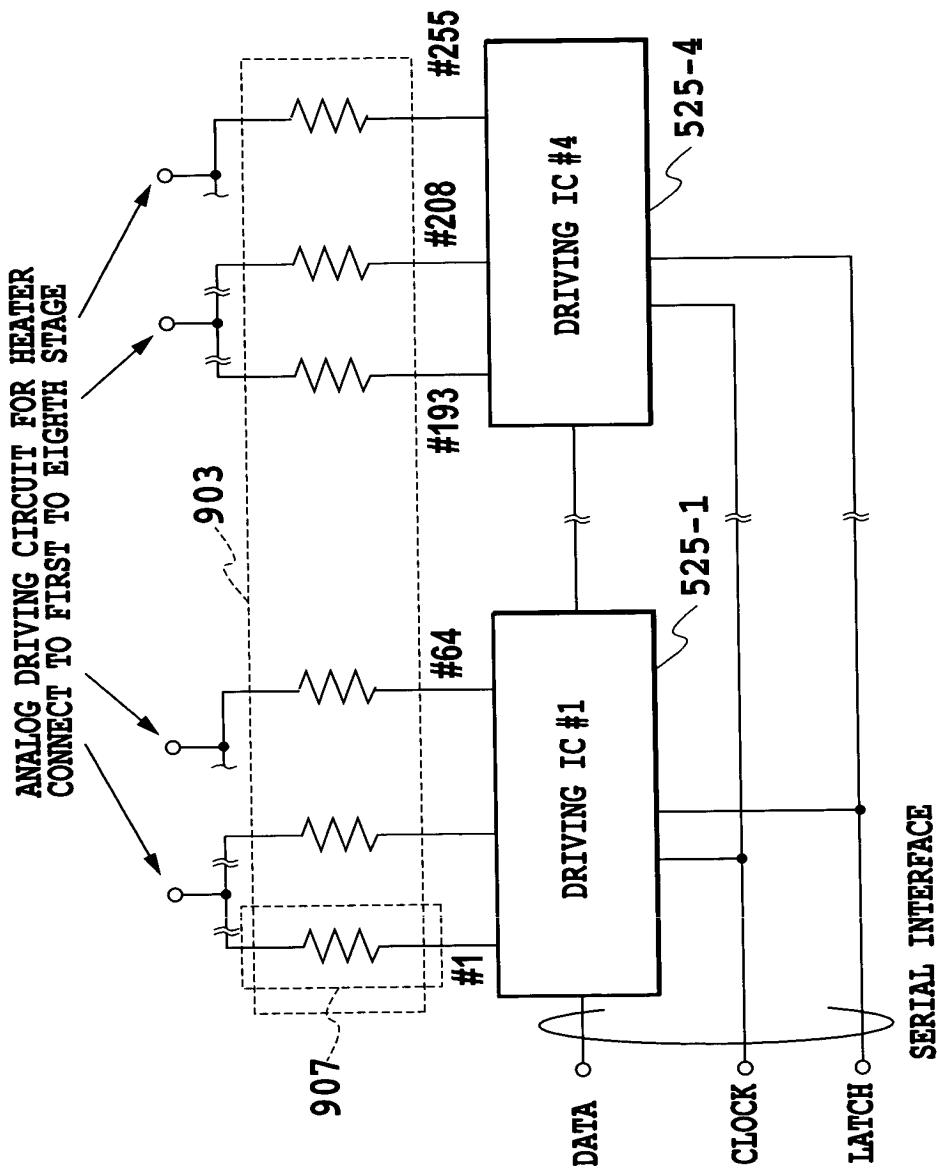


FIG.14

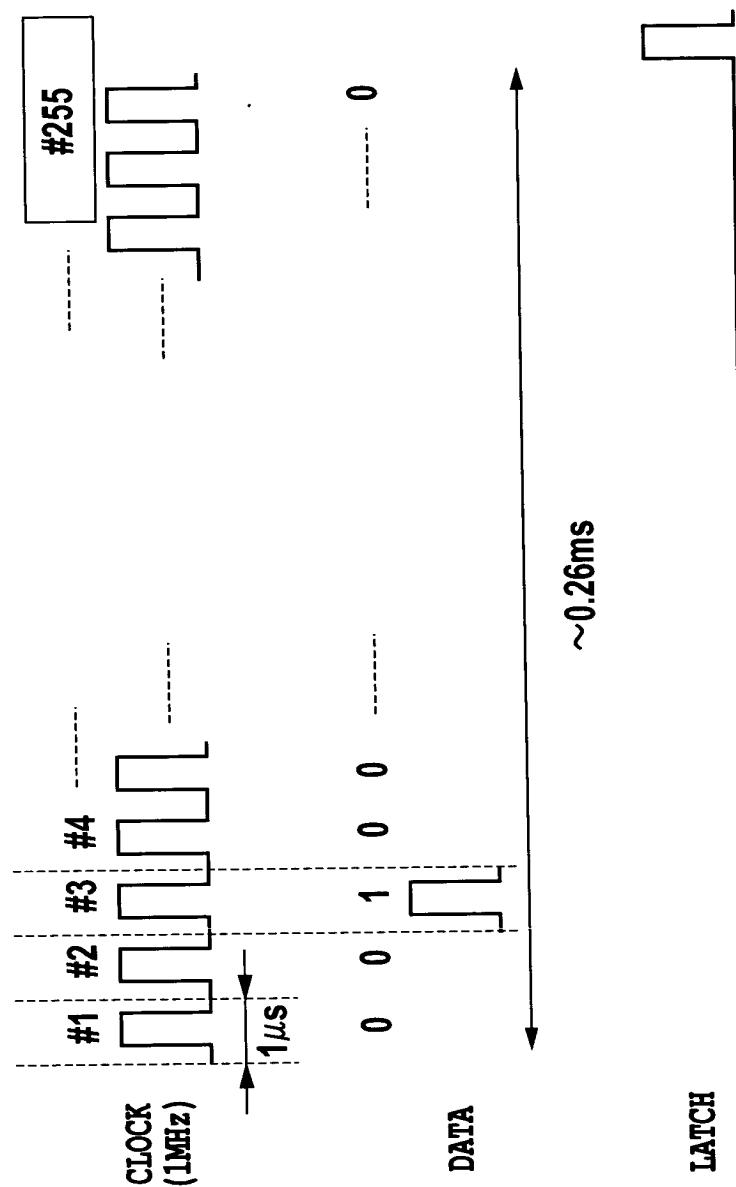
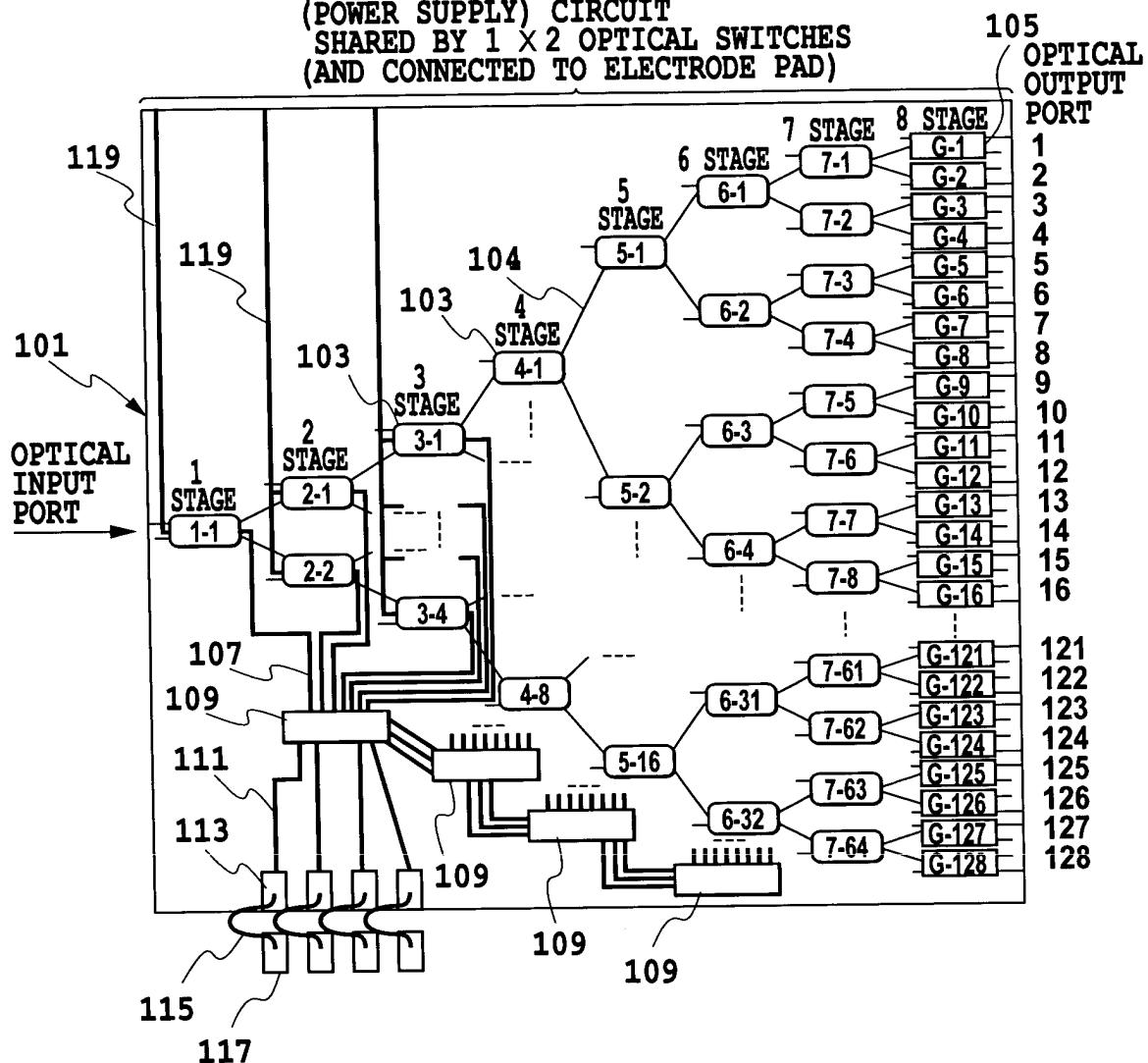


FIG.15

(8 LINES) CONNECTED TO DRIVING  
(POWER SUPPLY) CIRCUIT  
SHARED BY 1 X 2 OPTICAL SWITCHES  
(AND CONNECTED TO ELECTRODE PAD)



**FIG. 16**

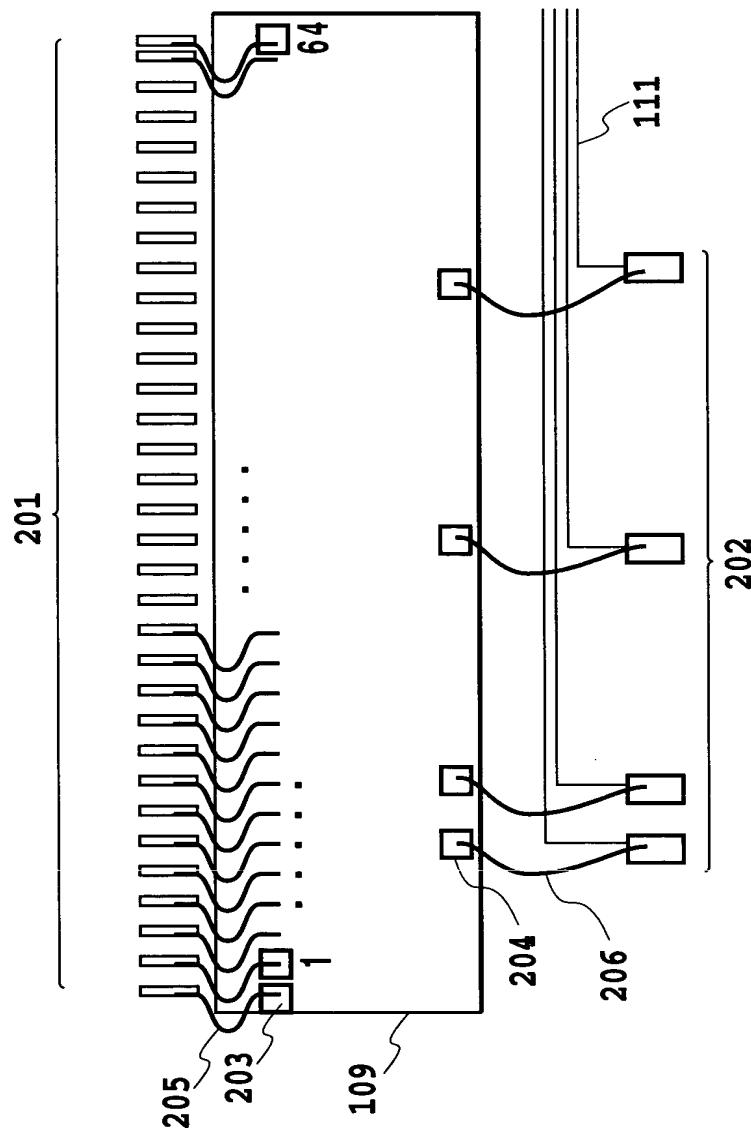


FIG. 17

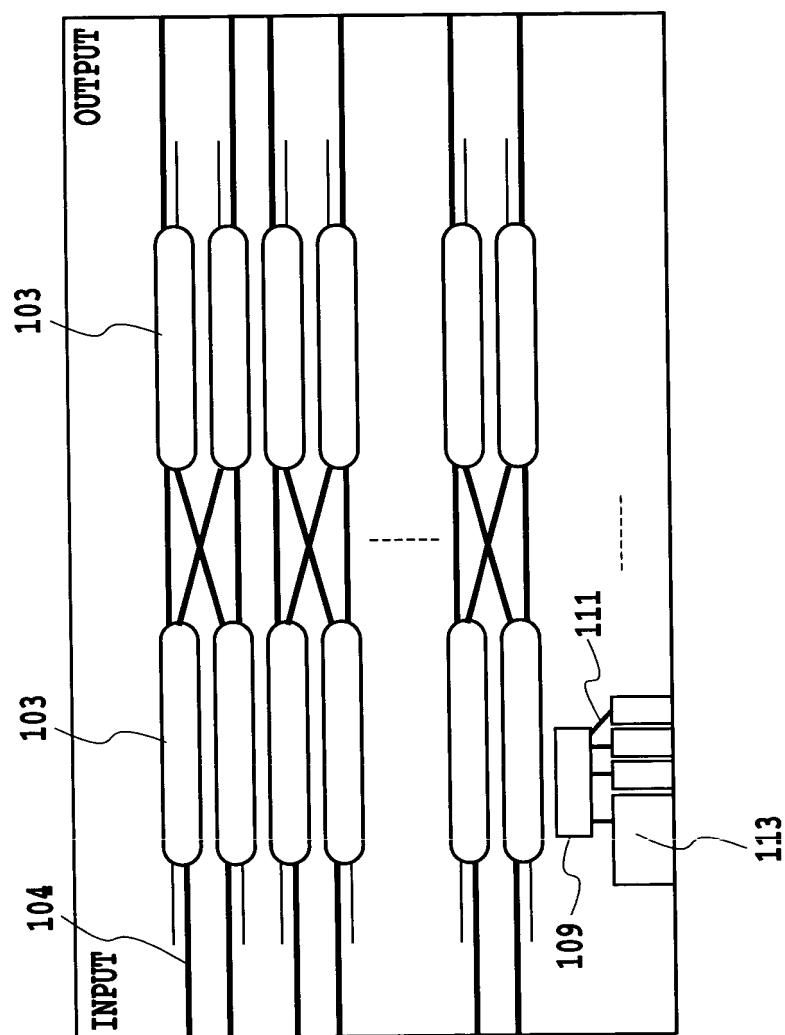


FIG.18

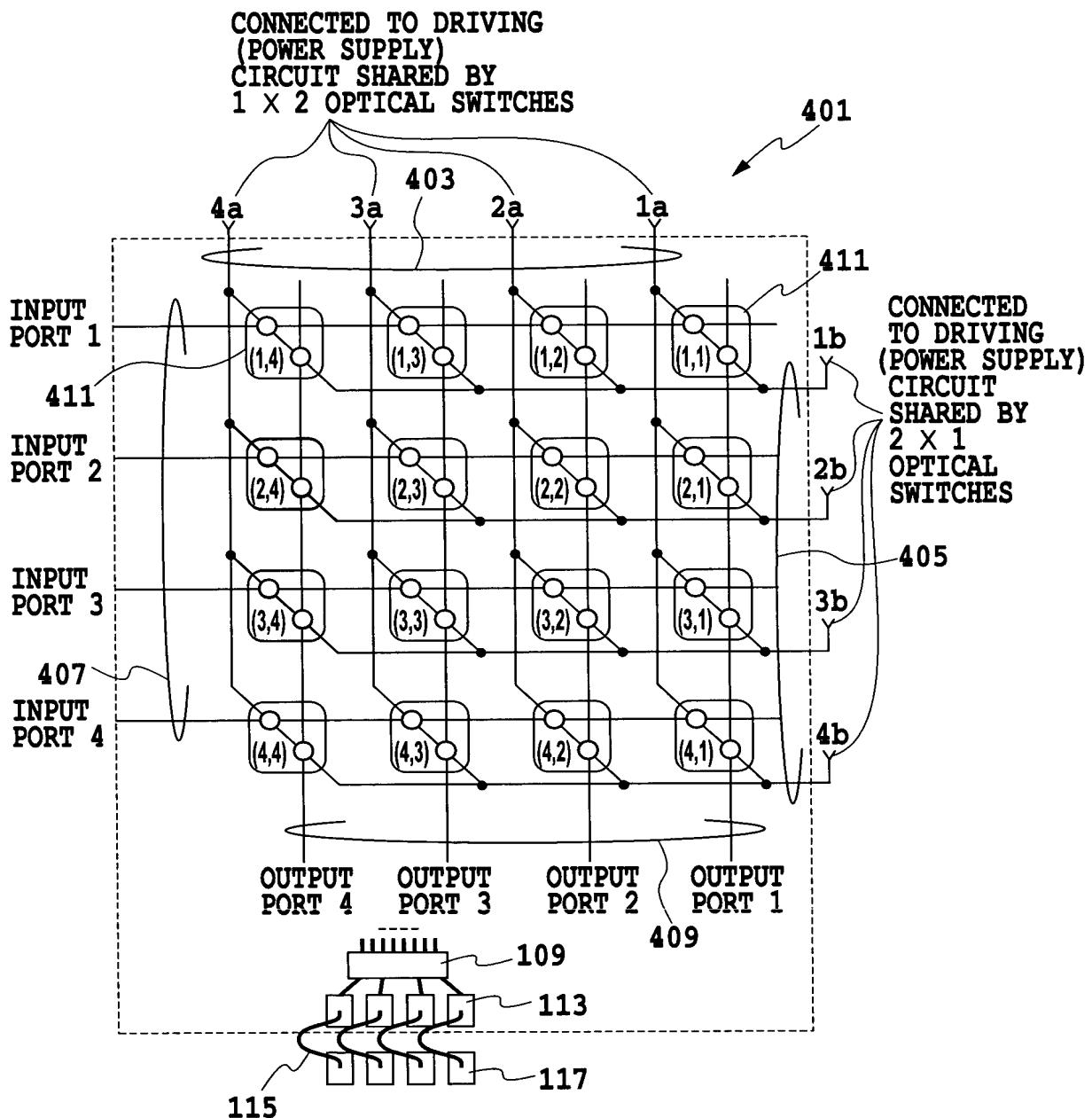
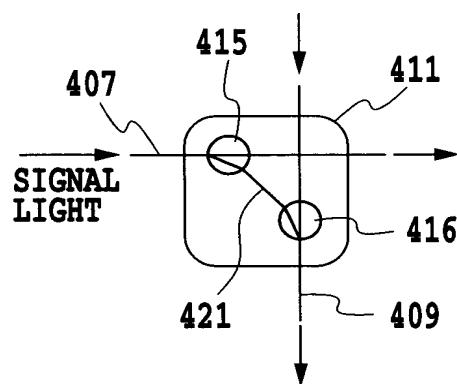
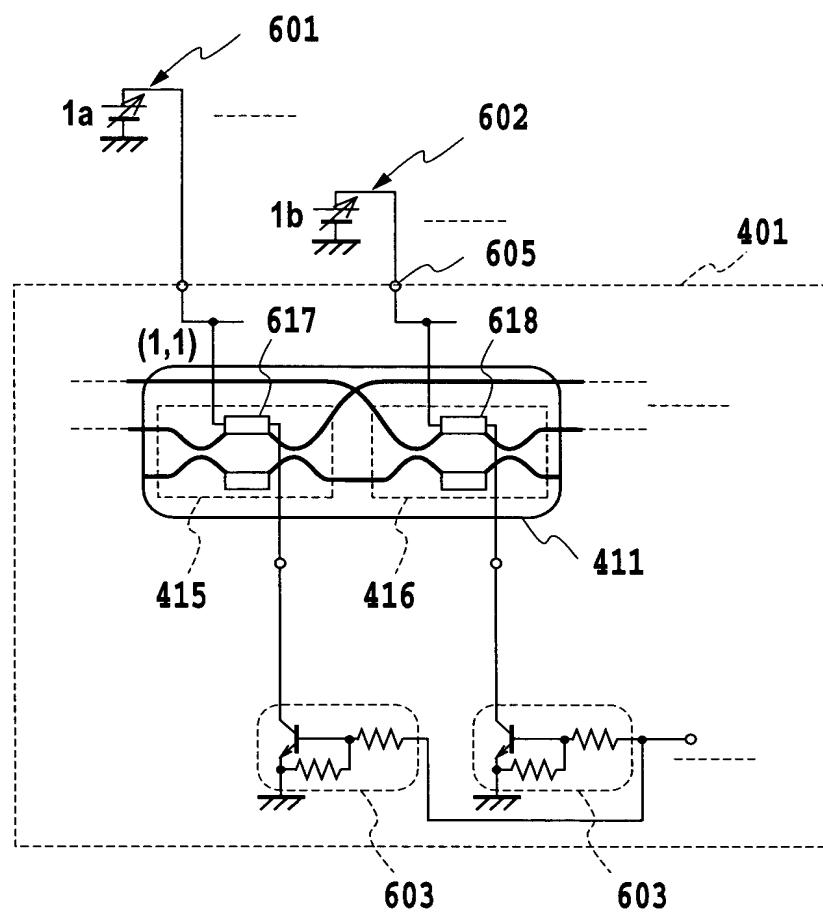


FIG.19



**FIG.20**



**FIG.21**

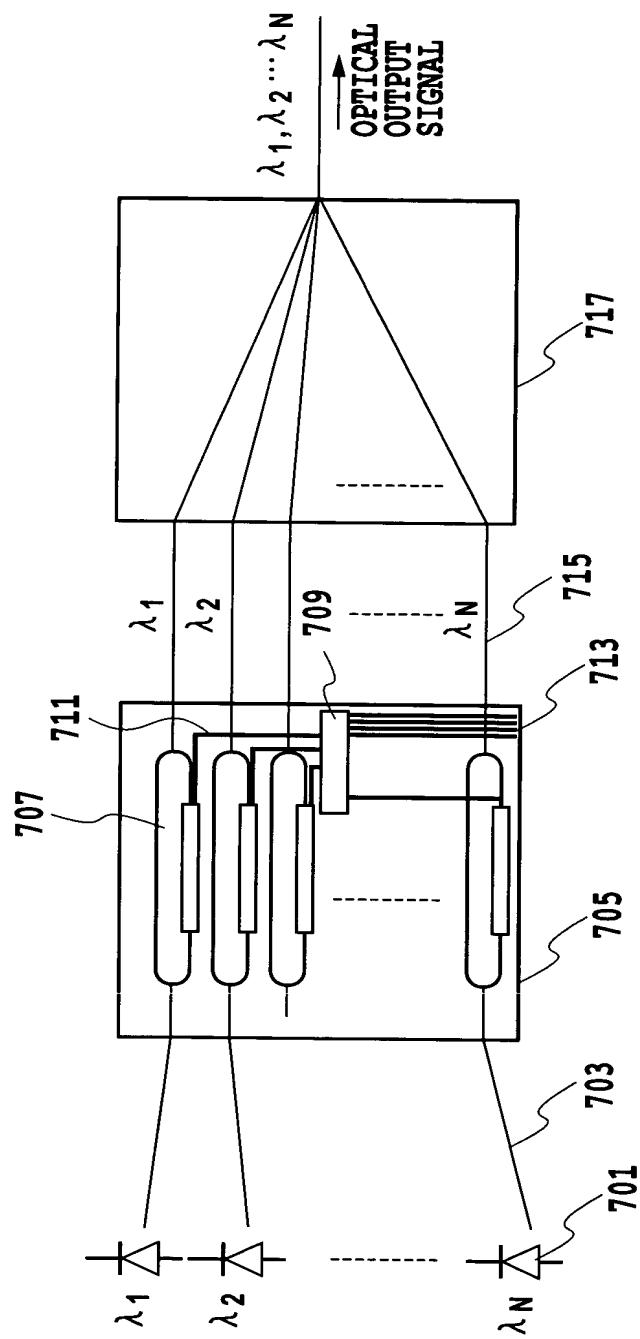
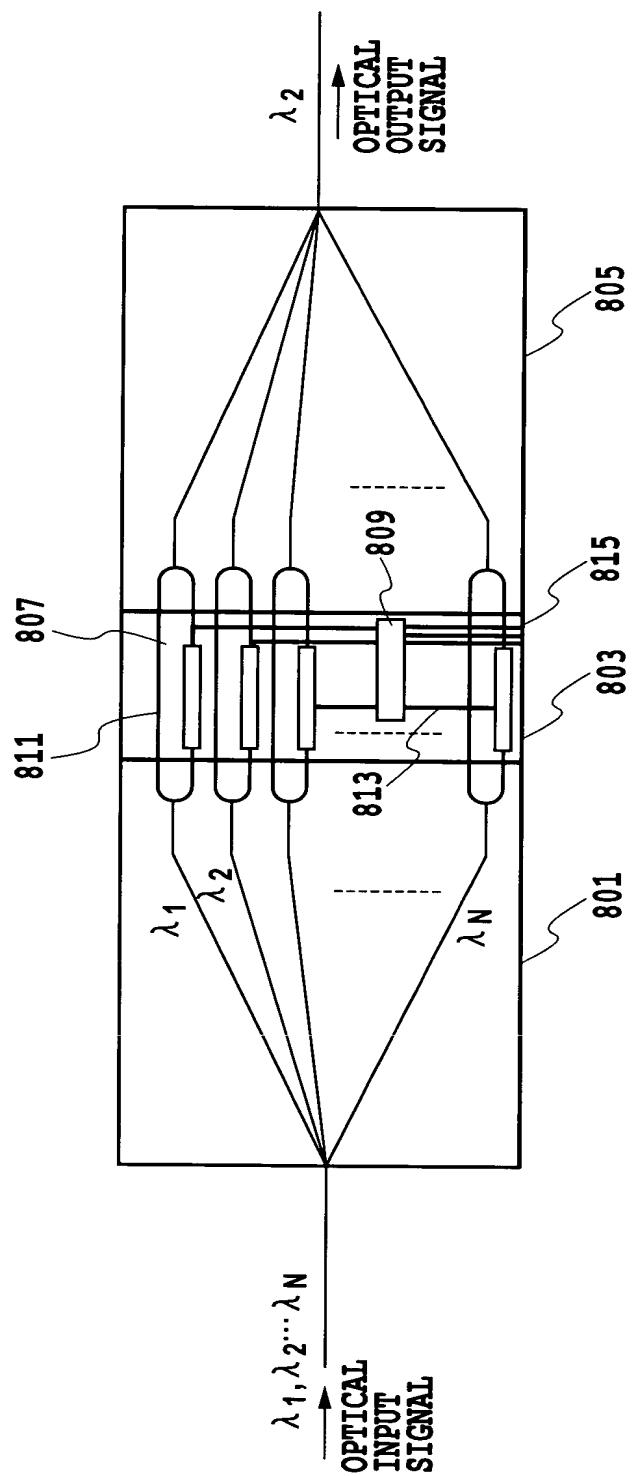


FIG.22



**FIG.23**